



## Internship: Analog- and Mixed Signal Design

### Job description

We are looking for students (f/m/div)\* with the willingness to work in a challenging environment within a highly motivated international team. Apply now for this internship in Graz and support our team!

In your new role you will:

- Experience latest **R&D tools** and **methods** for analog circuit design
- Contribute to innovative **circuit solutions** needed for challenging requirements
- Undertake activities related to different steps in the **analog** and **mixed signal design flow**
- Develop and verify **analog circuits**
- Propose new flows or methods and **drive changes** including **pilot runs** and **implementations**
- Need to be innovative and need to “**think out of the box**”

Learning outcomes:

- Be exposed to various steps in **design** and **verification in the chip development flow**
  - Learn about specific requirements for **automotive applications** and **circuits**
  - Gather detailed knowledge of **analog power circuits**
  - Acquire details of **ESD** and **EMC** robust solutions
  - Obtain knowledge about details and challenges of a **BCD frontend process**
  - **Improve communication** and **interpersonal skills** through articulating the impact of proposed changes to all major stakeholders
- Learn how to deliver a project in a **timely manner** and work effectively in an **international team**

#### Further Information:

Type of employment: Temporary / Part-time (flexible working hours from Monday to Friday between 6 a.m. and 7 p.m.)

Duration: min. 6 months

### At a glance

Location: **Graz (Austria)**  
Job ID: **337359**  
Start date: **Mar 01, 2022**  
Entry level: **0-1 year**  
Type: **Part time**  
Contract: **Temporary**

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### Contact

**Nico Steinhauser**

Talent Attraction Manager

## Profile

You are best equipped for this task if you have:

- Achieved Bachelor degree in **Electrical Engineering** or **comparable study**, now on track for the Master's degree
- Familiar with the **basics of analog circuit design**. Ideally you attended courses in analog circuit design in your high school or university
- Basic knowledge of **analog circuit simulation tools** (PSpice, Cadence design suit or similar)
- Proficient in **English, German** advantageous
- Like to **work in a team**

This position is subject to the collective agreement for workers and employees in the electrical and electronics industry (full-time) employment group D for master students (<https://www.feei.at/leistungen/informations-service/mindestloehne-und-gehalter-2020>).

**Please attach the following documents (German or English) to your application:**

- Motivation letter
- CV
- Certificate of matriculation at a university
- Transcript of records
- Highest completed educational certificate (Matura certificate for Bachelor students, Bachelor certificate for Master students)
- Reference letter